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		Subclass	Classification
		Class	Issue

**U.S. UTILITY Patent Application**

O.I.P.E. PATENT DATE  
S. P. E. D. I. S. Q. A. A.M.

APPLICATION NO. 09/878554	CONT/PRIOR	CLASS 714	SUBCLASS 741	ART UNIT 2133	EXAMINER <i>Decady</i>
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RSD

Efficiency of fault simulation by logic backtracking

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# **Best Available Copy**

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
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